

PRIOR ART

FIG. 1

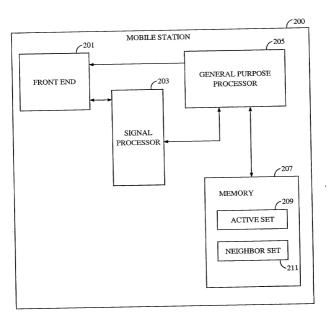
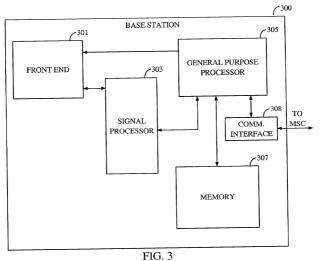


FIG. 2



TO BASE STATIONS COMMUNICATION INTERFACE GENERAL PURPOSE PROCESSOR

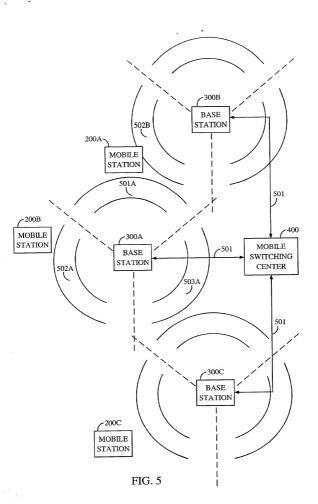
MEMORY

400

403

GENERAL PURPOSE PROCESSOR

FIG. 4



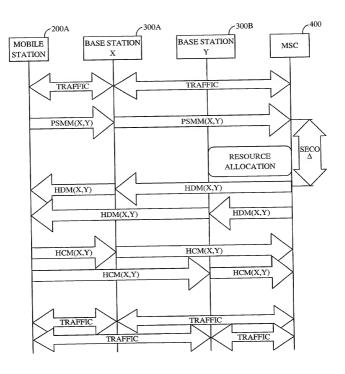


FIG. 6

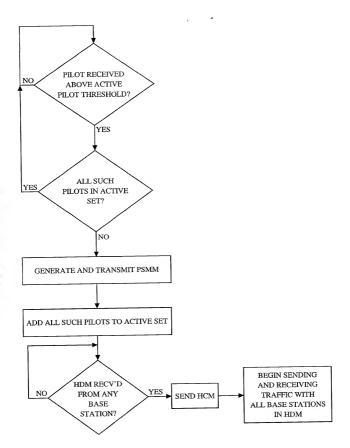


FIG. 7

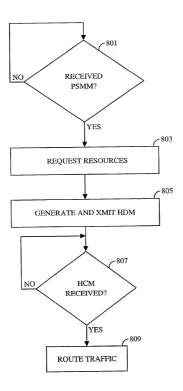


FIG. 8